



09/852,839

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KONDO ET AL. Examiner: V Nguyen
Serial No.: 09/852,839 Group Art Unit: 2858
Filed: May 10, 2001 Docket No.: 10873.713US01
Title: SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE
VOLTAGE GENERATION CIRCUIT, TEST METHOD FOR THE SAME,
AND RECORDING DEVICE AND COMMUNICATION EQUIPMENT
HAVING THE SAME

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service, as first class mail, with sufficient postage, in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 7/09/03.

By:

Name:

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Dear Sir:

In response to the Office Action of April 29, 2003, please enter the following amendments and remarks:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of the claims which begins on page 3 of this paper.

Amendments to the Drawings begin on page 11 of this paper and include both attached replacement sheets and annotated sheets showing changes.

Remarks begin on page 12 of this paper.

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